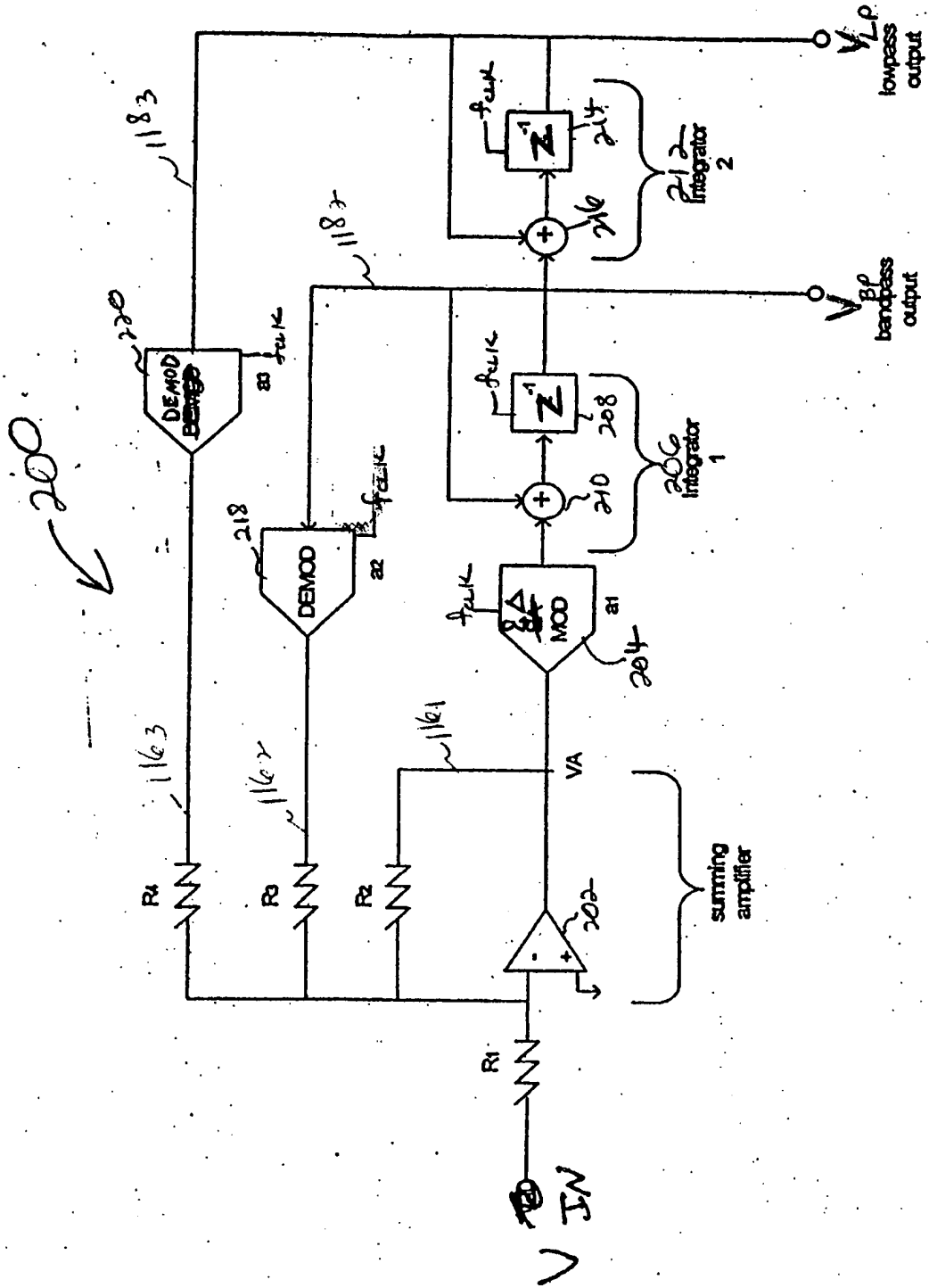


FIG. 1

FIG. 2



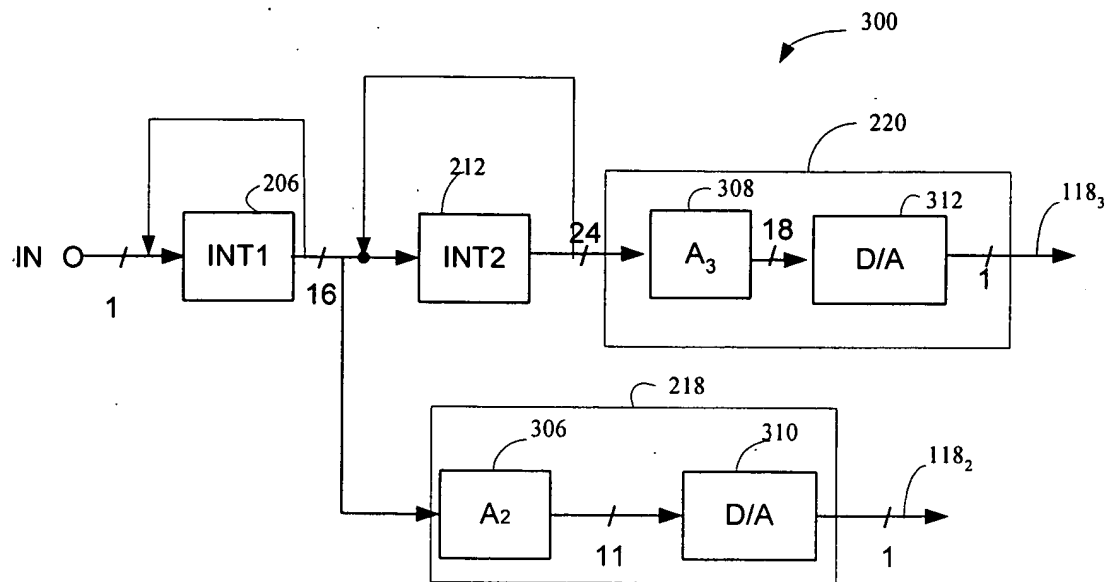
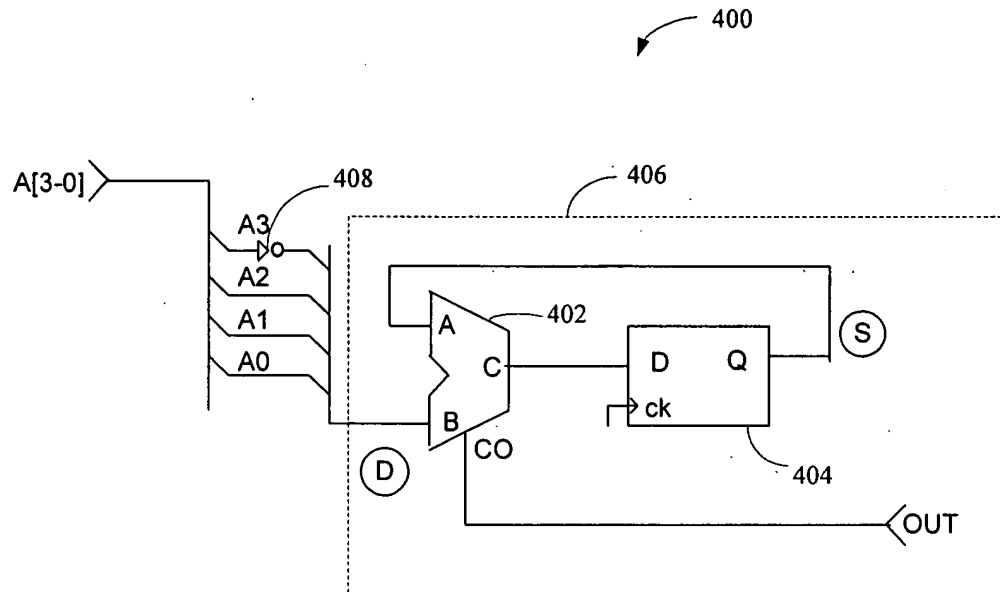


Fig. 3



Input: two's complement
 Output: one bit
 HI = + 5V
 LO = - 5V

Fig. 4

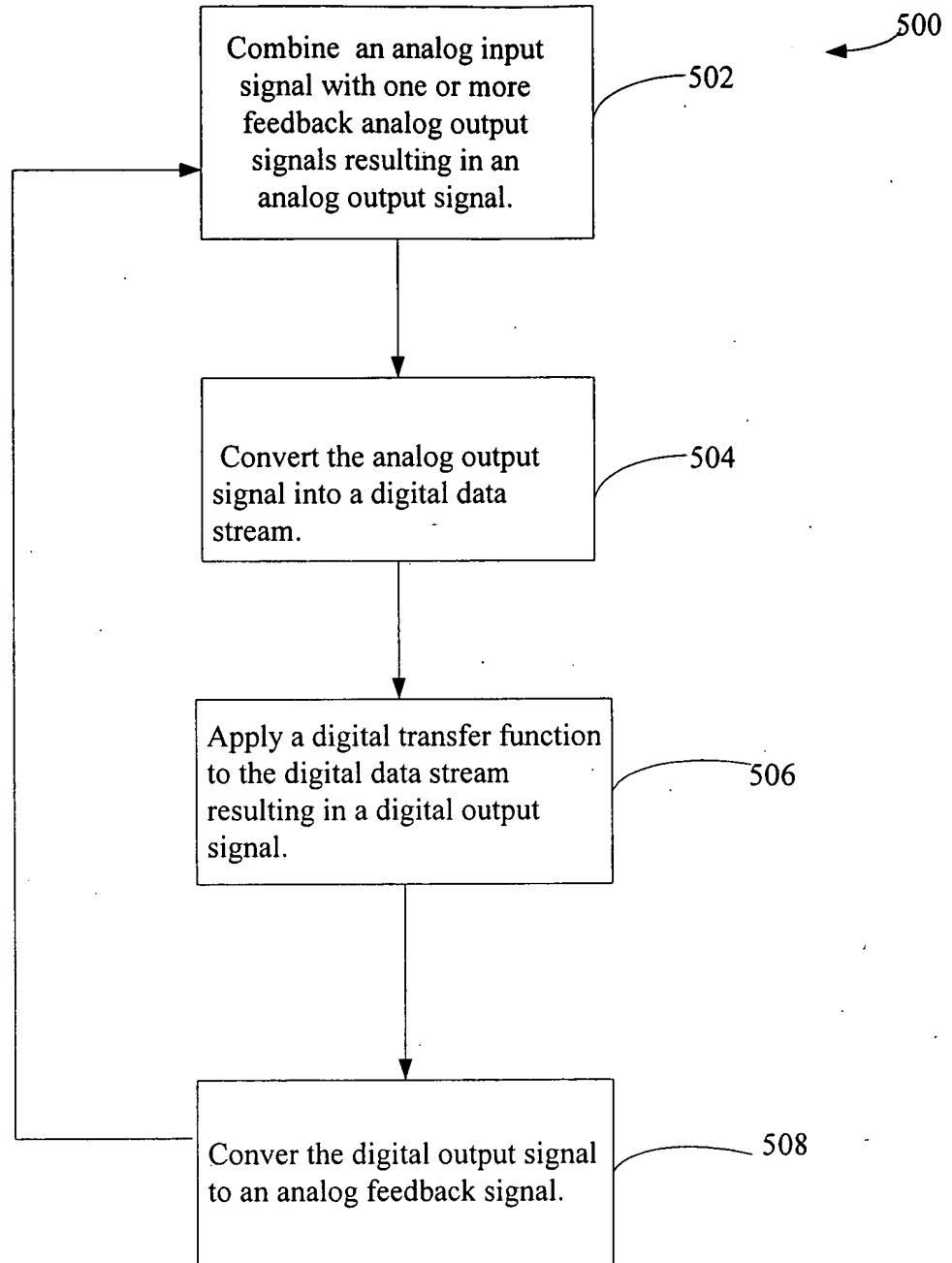


FIG. 5